

Abstracts

A highly integrated radio transceiver chipset for DECT

J. Strang and S. Atkinson. "A highly integrated radio transceiver chipset for DECT." 1997 Radio Frequency Integrated Circuits (RFIC) Symposium 97. (1997 [RFIC]): 131-134.

A two chip solution for the DECT (Digital Enhanced Cordless Telecommunications) system has been implemented using Si bipolar technologies. A front end device implements up and down converting mixers, a VCO, driver amplifiers etc. Using a high speed 24-GHz NPN bipolar process. The IF device includes a down converting mixer, VCO, IF limiting strip with RSSI, on chip band-pass Gm-C filters and a fully integrated PLL demodulator. This device is implemented using a high speed complementary bipolar process. Both IC's include on-chip voltage regulation and power management.

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